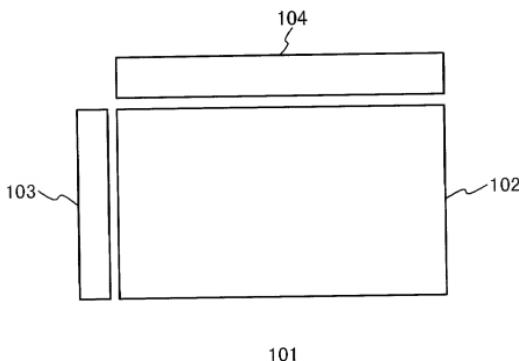
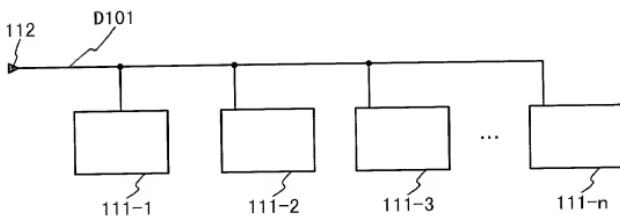


Fig. 1 PRIOR ART



019893598 - 052901

Fig. 2 PRIOR ART



104

706290 * 86556860

Fig. 3 PRIOR ART

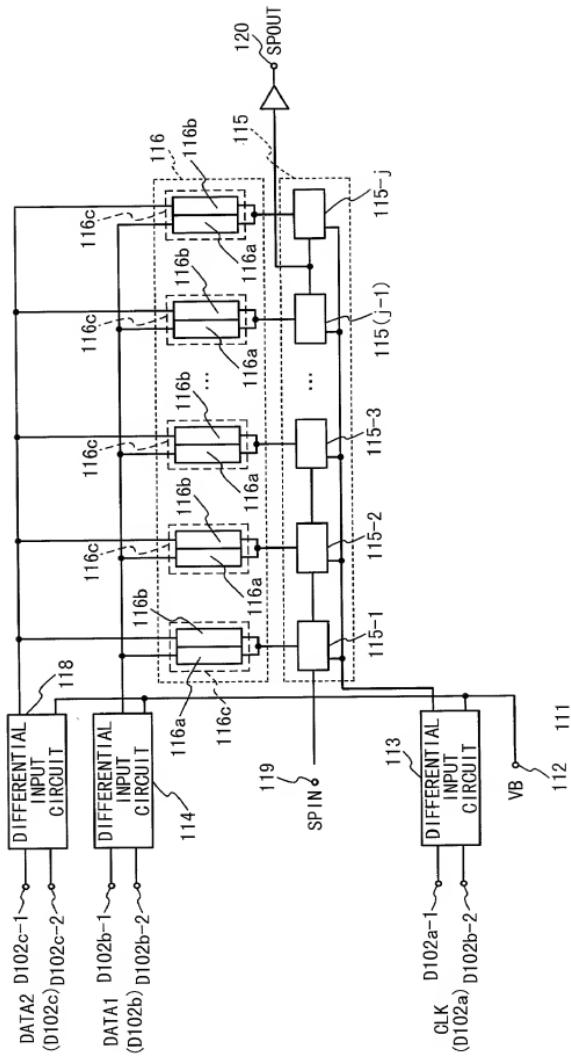


Fig. 4

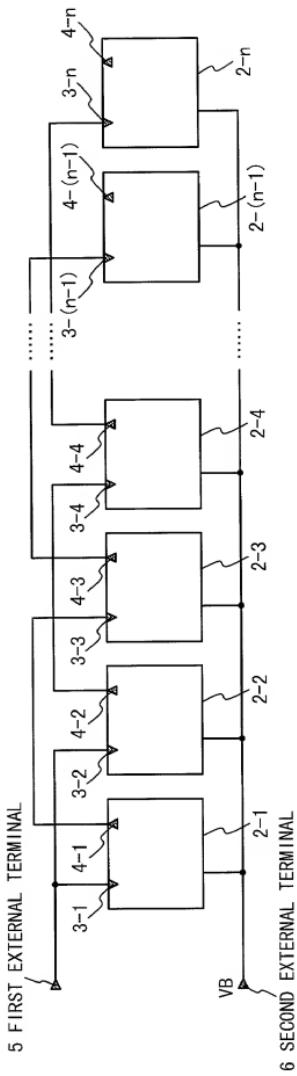


Fig. 5A

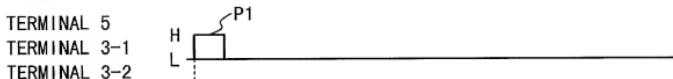


Fig. 5B

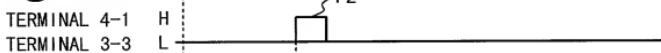


Fig. 5C



Fig. 5D

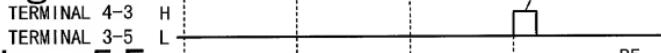


Fig. 5E



Fig. 5F

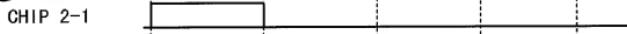


Fig. 5G

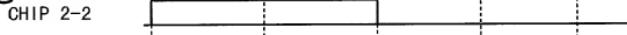


Fig. 5H



Fig. 5I

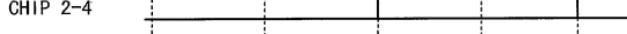


Fig. 5J



Fig. 5K



Fig. 6

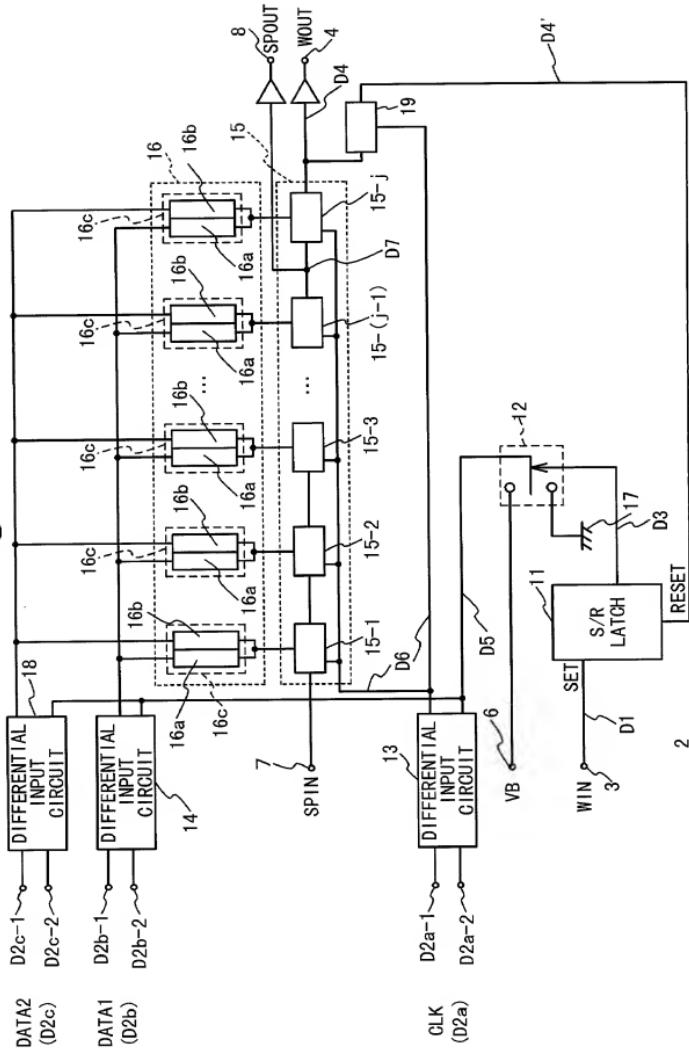
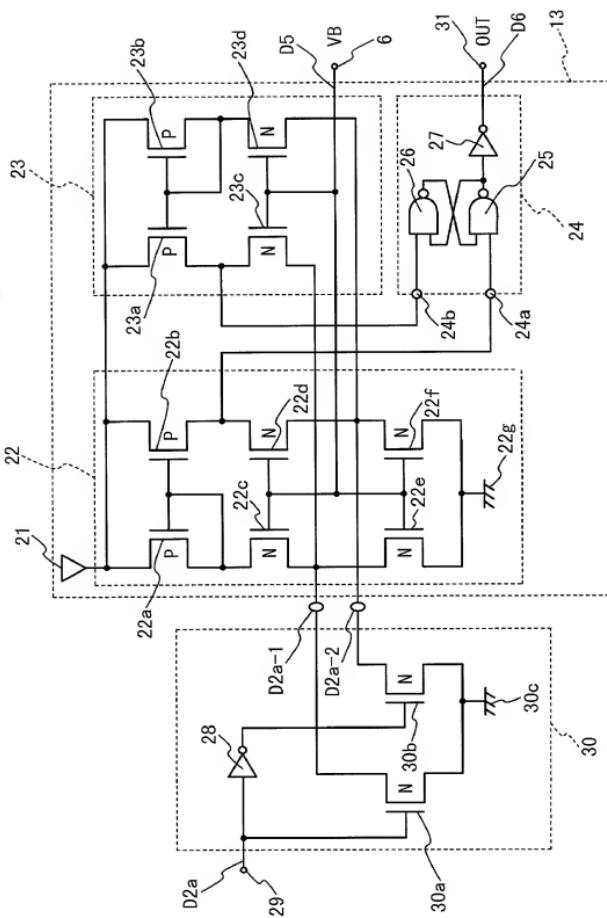


Fig. 7



8

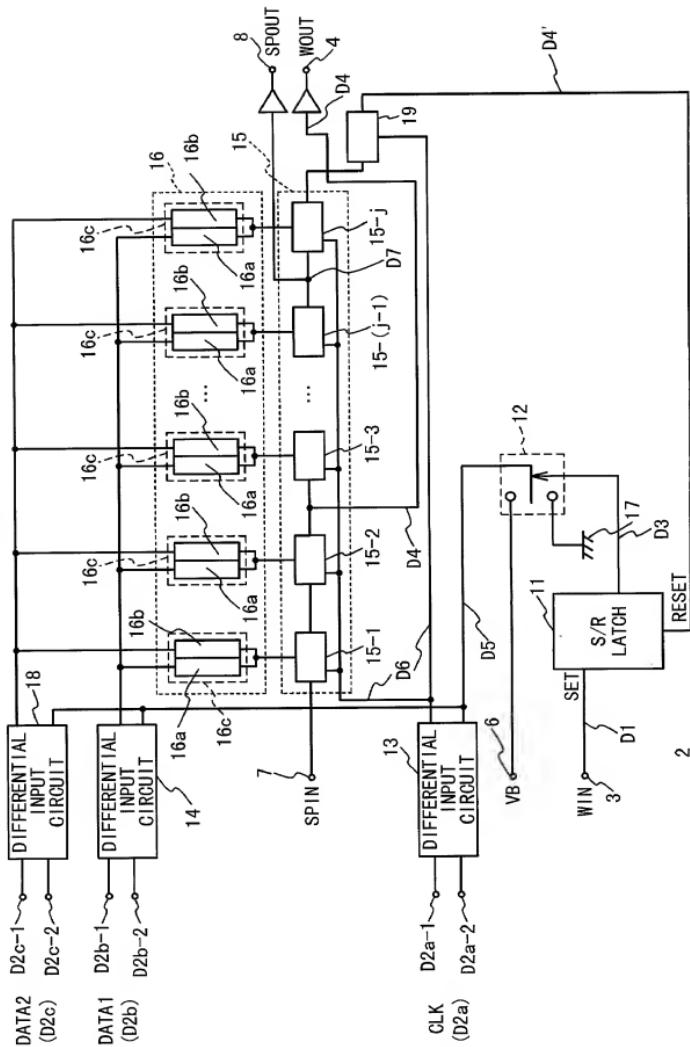


Fig. 9A

TERMINAL 3

H

L

Fig. 9B

CONTROL SIGNAL D3

V_A

0

Fig. 9C

CIRCUIT 13

H

L

Fig. 9D

CIRCUIT 14

H

L

Fig. 9E

CIRCUIT 18

H

L

Fig. 9F

TERMINAL 4

H

L

Fig. 9G

CIRCUIT 19

H

L

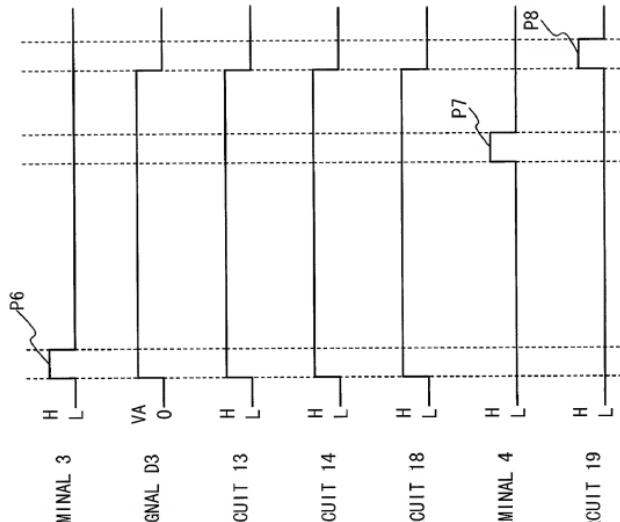


Fig. 10

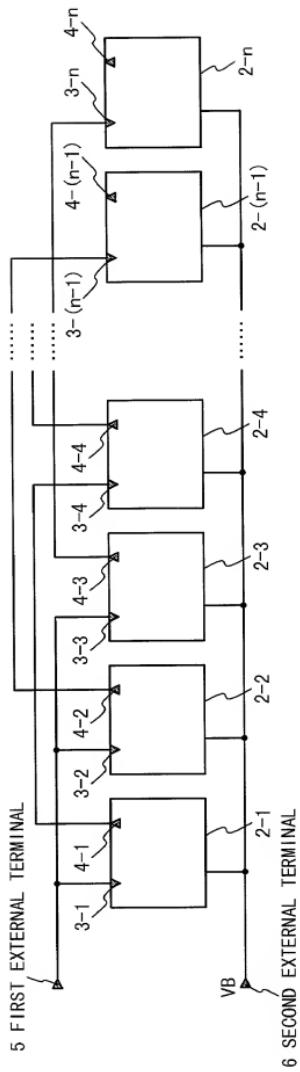


Fig. 11A



Fig. 11B



Fig. 11C

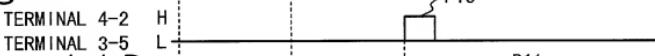


Fig. 11D

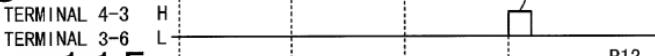


Fig. 11E

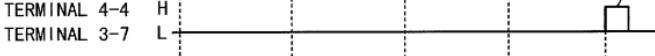


Fig. 11F

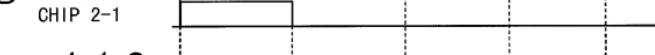


Fig. 11G



Fig. 11H

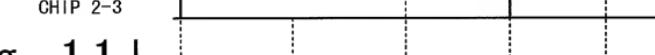


Fig. 111



Fig. 11J
CHIP 2-5



Fig. 11K
CHIP 2-6

